

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Yasutoshi AIBARA et al.

Appln. No.: 10/080,586 Group Art Unit: 2878

Filed: February 25, 2002 Examiner: C. Glass

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND

IMAGING SYSTEM

AMENDMENT

Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

Sir:

In response to the Office Action mailed May 8, 2003, please amend the above-identified patent application as indicated below.

Amendments to the specification begin on page 2 of this paper.

Amendments to the drawings begin on page 3 of this paper and include an attached replacement sheet.

Amendments to the claims are reflected in the listing of claims which begins on page 4 of this paper.

Remarks/Arguments begin on page 9 of this paper.